



PCF50633 variant 05

Options for PCF50633HN/05/N3

IBCP-DS-002560 R-

Rev 0.2 – 8 January 2007

Variant Specification

1a Type information

PMU	PCF50633
Silicon version	N3
Variant	05
Bond out	n.a.
Product specification ¹	t.b.f.

1b Document revision history

Ver	Date	Description	Author
0.2	08-Jan-2007	Change maxcur setting for DCDC converters	MLA
0.1	23-Oct-2006	Initial version	MLA

Note: in this variant 05 the pull-down resistors from the DOWN1 and AUTO DCDC converters are disabled.

¹ For reset settings is referred to the datasheet revision; unless otherwise specified

2 ON/OFF control (OOC)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
OOCMODE (10 _{HEX})	EXTON1_MODE (1,0)	EXTON1 mode selection 00: Wake-up by falling edge only 01: Wake-up by rising edge only 10: Wake-up by falling edge, rising edge sets the time-out timer to 8 seconds 11: Wake-up by rising edge, falling edge sets time-out timer to 8 seconds	01 (Wake-up by rising edge only)
	EXTON2_MODE (3,2)	EXTON2 mode selection Behaviour like EXTON1_MODE	11 (Wake-up by rising edge, falling edge sets time-out timer to 8 seconds)
	EXTON3_MODE (5,4)	EXTON3 mode selection Behaviour like EXTON1_MODE	00 (Wake-up by falling edge only)
	ONKEY_MODE (7,6)	ONKEY mode selection 00: Wake-up by falling edge only 01: Wake-up by falling edge, next 1 sec. low sets the time-out timer to 8 seconds. 10: Wake-up by falling edge, rising edge sets the time-out timer to 8 seconds. 11: Reserved	00 (Wake-up by falling edge only)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
OOCCTL (11 _{HEX})	ACTPHRST (1,0)	Selects activity phase to which host controller reset is related 00: related to activation phase 1 01: related to activation phase 2 10: related to activation phase 3 11: related to activation phase 4	11 (Related to activation phase 4)
	ACTCLK32ON (2)	Enables the 32kHz output in ACTIVE state	1 (32kHz output is enabled in ACTIVE state)
	HEARTBEAT (3)	Enables the Heartbeat mode for the KEEPACT input	0 (Heartbeat mode is disabled)
	USBBATCHK (4)	Enables Vbat > VbatOK condition for transition to ACTIVE state	0 (USBBATCHK is disabled)
	STBYCLK32ON (6)	Enables the 32kHz output in STANDBY state	1 (32kHz output is enabled in STANDBY state)

3 General Purpose In/Outputs (GPIO)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
GPIOCTL (13 _{HEX})	GPIO1DIR (0)	Direction of GPIO1: 0: Output, 1:Input	1 (Input)
	GPIO2DIR (1)	Direction of GPIO2: 0: Output, 1:Input	1 (Input)
	GPIO3DIR (2)	Direction of GPIO3: 0: Output, 1:Input	1 (Input)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
GPIO1CFG (14 _{HEX})	GPIO1SEL (2,0)	GPIO1 output signal selection 000: fixed '0' 001: Reserved 010: SYS pin voltage > V _{SYS} OK 011: battery charging in progress 100: mobile mode: Adapter & USB absent 101: USB pin voltage > V _{USBPRES} 110: activation phase 1 signal 111: fixed '1'	000 (Fixed '0')
	GPIOPOL (3)	GPIO1 output signal polarity: 0: no inversion, 1: inversion	0 (No inversion)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
GPIO2CFG (15 _{HEX})	GPIO2SEL (2,0)	GPIO2 output signal selection 000: fixed '0' 001: Reserved 010: SYS pin voltage > V _{SYS} OK 011: battery charging in progress 100: mobile mode: Adapter & USB absent 101: USB pin voltage > V _{USBPRES} 110: activation phase 2 signal 111: fixed '1'	000 (Fixed '0')
	GPIOPOL (3)	GPIO2 output signal polarity: 0: no inversion, 1: inversion	0 (No inversion)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
GPIO3CFG (16 _{HEX})	GPIO3SEL (2,0)	GPIO3 output signal selection 000: fixed '0' 001: Reserved 010: SYS pin voltage > V _{SYS} OK 011: battery charging in progress 100: mobile mode: Adapter & USB absent 101: USB pin voltage > V _{USBPRES} 110: activation phase 3 signal 111: fixed '1'	000 (Fixed '0')
	GPIOPOL (3)	GPIO3 output signal polarity: 0: no inversion, 1: inversion	0 (No inversion)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
GPOCFG (17 _{HEX})	GPOSEL (2,0)	GPO output signal selection 000: fixed '0' 001: LED external NFET drive signal 010: SYS pin voltage > V _{SYSOK} 011: battery charging in progress 100: mobile mode: Adapter & USB absent 101: USB pin voltage > V _{USBPRES} 110: activation phase 4 signal 111: fixed '1'	000 (Fixed '0')
	GPOPOL (3)	GPO output signal polarity: 0: no inversion, 1: inversion	0 (No inversion)

4 Voltage monitors

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
BVMCTL (18 _{HEX})	BVMTHRSHLD (3,1)	Threshold voltage 000: n.a. 001: 2.80V 010: 2.90V 011: 3.00V 100: 3.10V 101: 3.20V 110: 3.30V 111: 3.40V	100 (3.10V)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
SVMCTL (19 _{HEX})	SVMTHRSHLD (3,1)	Threshold voltage 000: n.a. 001: 2.80V 010: 2.90V 011: 3.00V 100: 3.10V 101: 3.20V 110: 3.30V 111: 3.40V	011 (3.00V)

5 AUTO, DOWN1 and DOWN2 switching regulators

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
AUTOOUT (1A _{HEX})	AUTO_OUT (7,0)	Output voltage setting 00100011: 1.500V 00100100: 1.525V 00100101: 1.550V 00100110: 1.575V 00100111: 1.600V ... 00101111: 1.800V ... 01010011: 2.700V ... 01101010: 3.275V 01101011: 3.300V 01101100: 3.325V ... 10101111: 5.000V ... 10111110: 5.375V 10111111: 5.400V 11111111: 5.400V	01101011 (3.300Volt)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
AUTOENA (1B _{HEX})	AUTO_ON (0)	If set, converter is on	1 (Set, converter is ON)
	AUTO_P1C (1)	If set, converter is on when GPIO1 = 1	0 (Not set)
	AUTO_P2C (2)	If set, converter is on when GPIO2 = 1	0 (Not set)
	AUTO_P3C (3)	If set, converter is on when GPIO3 = 1	0 (Not set)
	AUTO_ENA_ACT (5,4)	Selects activation phase 00: Phase 1 01: Phase 2 10: Phase 3 11: Phase 4	00 (Activation phase 1 selected)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
AUTOMXC (1D _{HEX})	AUTO_MAXC (5,0)	Sets current limit of AUTO converter lin, max = AUTO_MAXC * 40mA	001010 (Current limiter set for 400mA)
	AUTOMAXCMOD (6)	Current limit mode: 0: limiting at start-up only 1: limiting always active	0 (Limiting at start-up only)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
DOWN1OUT (1E _{HEX})	DOWN1_OUT (7,0)	Output voltage setting 00000000: 0.625V 00000001: 0.650V 00000010: 0.675V ... 00010110: 1.175V 00010111: 1.200V 00011000: 1.225V ... 00100011: 1.500V ... 00101111: 1.800V ... 01010011: 2.700V ... 01101010: 3.275V 01101011: 3.300V 01101100: 3.325V ... 10101111: 5.000V ... 10111110: 5.375V 10111111: 5.400V	00010111 (1.200Volt)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
DOWN1ENA (1F _{HEX})	DOWN1_ON (0)	If set, converter is on	1 (Set, converter is ON)
	DOWN1_P1C (1)	If set, converter is on when GPIO1 = 1	0 (Not set)
	DOWN1_P2C (2)	If set, converter is on when GPIO2 = 1	0 (Not set)
	DOWN1_P3C (3)	If set, converter is on when GPIO3 = 1	0 (Not set)
	DOWN1_ENA_ACT (5,4)	Selects activation phase 00: Phase 1 01: Phase 2 10: Phase 3 11: Phase 4	10 (Activation phase 3 selected)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
DOWN1MXC (21 _{HEX})	DOWN1_MAXC (5,0)	Sets current limit of DOWN1 converter lin, max = DOWN1_MAXC * 15mA	011010 (Current limiter set for 390mA)
	DOWN1MAXCMOD (6)	Current limit mode: 0: limiting at start-up only 1: limiting always active	0 (Limiting at start-up only)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
DOWN2OUT (22 _{HEX})	DOWN2_OUT (7,0)	Output voltage setting 00000000: 0.625V 00000001: 0.650V 00000010: 0.675V ... 00010110: 1.175V 00010111: 1.200V 00011000: 1.225V ... 00100011: 1.500V ... 00101111: 1.800V ... 01010011: 2.700V ... 01101010: 3.275V 01101011: 3.300V 01101100: 3.325V ... 10101111: 5.000V ... 10111110: 5.375V 10111111: 5.400V	00010111 (1.200Volt)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
DOWN2ENA (23 _{HEX})	DOWN2_ON (0)	If set, converter is on	1 (Set, converter is ON)
	DOWN2_P1C (1)	If set, converter is on when GPIO1 = 1	0 (Not set)
	DOWN2_P2C (2)	If set, converter is on when GPIO2 = 1	0 (Not set)
	DOWN2_P3C (3)	If set, converter is on when GPIO3 = 1	0 (Not set)
	DOWN2_ENA_ACT (5,4)	Selects activation phase 00: Phase 1 01: Phase 2 10: Phase 3 11: Phase 4	10 (Activation phase 3 selected)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
DOWN2MXC (25 _{HEX})	DOWN2_MAXC (5,0)	Sets current limit of DOWN2 converter lin, max = DOWN2_MAXC * 15mA	011010 (Current limiter set for 390mA)
	DOWN2MAXCMOD (6)	Current limit mode: 0: limiting at start-up only 1: limiting always active	0 (Limiting at start-up only)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
MEMLDOOUT (26 _{HEX})	MEMLDO_OUT (4,0)	Output voltage setting: $0.9v + V_{SET} * 0.1V$ 00000: 0.9V 00001: 1.0V ... 11000: 3.3V ... 11011: 3.6V 11111: 3.6V	01001 (1.800Volt)
	MEMLDO_SW (5)	If set, switch mode is selected	0 (Not set, switch mode is OFF)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
MEMLDOENA (27 _{HEX})	MEMLDO_ON (0)	If set, regulator is on	0 (Not set, regulator is OFF)
	MEMLDO_P1C (1)	If set, regulator is on when GPIO1 = 1	0 (Not set)
	MEMLDO_P2C (2)	If set, regulator is on when GPIO2 = 1	0 (Not set)
	MEMLDO_P3C (3)	If set, regulator is on when GPIO3 = 1	0 (Not set)
	MEMLDO_ENA_ACT (5,4)	Selects activation phase 00: Phase 1 01: Phase 2 10: Phase 3 11: Phase 4	00 (Activation phase 1 selected)

6 Linear regulators

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
LDO1OUT (2D _{HEX})	LDO1_OUT (4,0)	Output voltage setting: $0.9V + V_{SET} * 0.1V$ 00000: 0.9V 00001: 1.0V ... 11000: 3.3V ... 11011: 3.6V 11111: 3.6V	00110 (1.5Volt)
	LDO1_SWMOD (5)	If set, switch mode is selected	0 (Not set, switch mode is OFF)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
LDO1ENA (2E _{HEX})	LDO1_ON (0)	If set, regulator is on	0 (Not set, regulator is OFF)
	LDO1_P1C (1)	If set, regulator is on when GPIO1 = 1	0 (Not set)
	LDO1_P2C (2)	If set, regulator is on when GPIO2 = 1	0 (Not set)
	LDO1_P3C (3)	If set, regulator is on when GPIO3 = 1	0 (Not set)
	LDO1_ENA_ACT (5,4)	Selects activation phase 00: Phase 1 01: Phase 2 10: Phase 3 11: Phase 4	00 (Activation phase 1 selected)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
LDO2OUT (2F _{HEX})	LDO2_OUT (4,0)	Output voltage setting: $0.9V + V_{SET} * 0.1V$ 00000: 0.9V 00001: 1.0V ... 11000: 3.3V ... 11011: 3.6V 11111: 3.6V	01001 (1.8Volt)
	LDO2_SWMOD (5)	If set, switch mode is selected	0 (Not set, switch mode is OFF)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
LDO2ENA (30 _{HEX})	LDO2_ON (0)	If set, regulator is on	0 (Not set, regulator is OFF)
	LDO2_P1C (1)	If set, regulator is on when GPIO1 = 1	0 (Not set)
	LDO2_P2C (2)	If set, regulator is on when GPIO2 = 1	0 (Not set)
	LDO2_P3C (3)	If set, regulator is on when GPIO3 = 1	0 (Not set)
	LDO2_ENA_ACT (5,4)	Selects activation phase 00: Phase 1 01: Phase 2 10: Phase 3 11: Phase 4	00 (Activation phase 1 selected)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
LDO3OUT (31 _{HEX})	LDO3_OUT (4,0)	Output voltage setting: $0.9V + V_{SET} * 0.1V$ 00000: 0.9V 00001: 1.0V ... 11000: 3.3V ... 11011: 3.6V 11111: 3.6V	10011 (2.8Volt)
	LDO3_SWMOD (5)	If set, switch mode is selected	0 (Not set, switch mode is OFF)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
LDO3ENA (32 _{HEX})	LDO3_ON (0)	If set, regulator is on	0 (Not set, regulator is OFF)
	LDO3_P1C (1)	If set, regulator is on when GPIO1 = 1	0 (Not set)
	LDO3_P2C (2)	If set, regulator is on when GPIO2 = 1	0 (Not set)
	LDO3_P3C (3)	If set, regulator is on when GPIO3 = 1	0 (Not set)
	LDO3_ENA_ACT (5,4)	Selects activation phase 00: Phase 1 01: Phase 2 10: Phase 3 11: Phase 4	00 (Activation phase 1 selected)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
LDO4OUT (33 _{HEX})	LDO4_OUT (4,0)	Output voltage setting: $0.9V + V_{SET} * 0.1V$ 00000: 0.9V 00001: 1.0V ... 10111: 3.2V 11111: 3.2V	01001 (1.8Volt)
	LDO4_SWMOD (5)	If set, switch mode is selected	0 (Not set, switch mode is OFF)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
LDO4ENA (34 _{HEX})	LDO4_ON (0)	If set, regulator is on	1 (Set, regulator is ON)
	LDO4_P1C (1)	If set, regulator is on when GPIO1 = 1	0 (Not set)
	LDO4_P2C (2)	If set, regulator is on when GPIO2 = 1	0 (Not set)
	LDO4_P3C (3)	If set, regulator is on when GPIO3 = 1	0 (Not set)
	LDO4_ENA_ACT (5,4)	Selects activation phase 00: Phase 1 01: Phase 2 10: Phase 3 11: Phase 4	01 (Activation phase 2 selected)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
LDO5OUT (35 _{HEX})	LDO5_OUT (4,0)	Output voltage setting: $0.9V + V_{SET} * 0.1V$ 00000: invalid 00001: 1.0V ... 10111: 3.2V 11111: 3.2V	01001 (1.8Volt)
	LDO2_SWMOD (5)	If set, switch mode is selected	0 (Not set, switch mode is OFF)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
LDO5ENA (36 _{HEX})	LDO5_ON (0)	If set, regulator is on	0 (Not set, regulator is OFF)
	LDO5_P1C (1)	If set, regulator is on when GPIO1 = 1	0 (Not set)
	LDO5_P2C (2)	If set, regulator is on when GPIO2 = 1	0 (Not set)
	LDO5_P3C (3)	If set, regulator is on when GPIO3 = 1	0 (Not set)
	LDO5_ENA_ACT (5,4)	Selects activation phase 00: Phase 1 01: Phase 2 10: Phase 3 11: Phase 4	00 (Activation phase 1 selected)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
LDO6OUT (37 _{HEX})	LDO6_OUT (4,0)	Output voltage setting: $0.9V + V_{SET} * 0.1V$ 00000: invalid 00001: 1.0V ... 11000: 3.3V ... 11011: 3.6V 11111: 3.6V	10011 (2.8Volt)
	LDO6_SWMOD (5)	If set, switch mode is selected	0 (Not set, switch mode is OFF)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
LDO6ENA (38 _{HEX})	LDO6_ON (0)	If set, regulator is on	1 (Set, regulator is ON)
	LDO6_P1C (1)	If set, regulator is on when GPIO1 = 1	0 (Not set)
	LDO6_P2C (2)	If set, regulator is on when GPIO2 = 1	0 (Not Set)
	LDO6_P3C (3)	If set, regulator is on when GPIO3 = 1	0 (Not set)
	LDO6_ENA_ACT (5,4)	Selects activation phase 00: Phase 1 01: Phase 2 10: Phase 3 11: Phase 4	00 (Activation phase 1 selected)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
HCLDOOUT (39 _{HEX})	HCLDO_OUT (4,0)	Output voltage setting: $0.9V + V_{SET} * 0.1V$ 00000: invalid 00001: 1.0V ... 10111: 3.2V 11111: 3.2V	11000 (3.3Volt)
	HCLDO_SWMOD (5)	If set, switch mode is selected	0 (Not set, switch mode is OFF)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
HCLDO ENA (3A _{HEX})	HCLDO_ON (0)	If set, regulator is on	0 (Not set, regulator is OFF)
	HCLDO_P1C (1)	If set, regulator is on when GPIO1 = 1	0 (Not set)
	HCLDO_P2C (2)	If set, regulator is on when GPIO2 = 1	0 (Not set)
	HCLDO_P3C (3)	If set, regulator is on when GPIO3 = 1	0 (Not set)
	HCLDO_ENA_ACT (5,4)	Selects activation phase 00: Phase 1 01: Phase 2 10: Phase 3 11: Phase 4	00 (Activation phase 1 selected)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
STBYCTL1 (3B _{HEX})	LDO1_ENA_STBY (0)	If set, LDO is on STANDBY state	0 (Not set)
	LDO2_ENA_STBY (2)	If set, LDO is on STANDBY state	0 (Not set)
	LDO3_ENA_STBY (4)	If set, LDO is on STANDBY state	0 (Not set)
	LDO4_ENA_STBY (6)	If set, LDO is on STANDBY state	0 (Not set)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
STBYCTL2 (3C _{HEX})	LDO5_ENA_STBY (0)	If set, LDO is on STANDBY state	0 (Not set)
	LDO6_ENA_STBY (2)	If set, LDO is on STANDBY state	1 (Set, regulator ON in STANDBY state)
	HCLDO_ENA_STBY (4)	If set, LDO is on STANDBY state	0 (Not set)
	MEMLDO_ENA_STBY (6)	If set, LDO is on STANDBY state	0 (Not set)

7 Main Battery Charger (MBC)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
MBCC1 (43 _{HEX})	CHGENA (0)	0: Charger disabled, Charger detection still active 1: Charger enabled	1 (Charger enabled)
	AUTOSTOP (1)	0: Automatic charge termination disabled. Charging is stopped by setting CHGENA or by time out of the watchdog timer 1: Automatic charging termination enabled. Charging is stopped when charging current falls below the CUTOFF current value or by time out of the watchdog timer	1 (Automatic charge termination enabled)
	AUTORES (2)	1 If set, automatic charge resume is enabled	0 (Set, automatic charge resume enabled)
	RESUME (3)	If set, charging is resumed. No effect when AUTORES = 1	0 (Not set)
	RESTART (4)	If set, charging process is restarted after halt condition	0 (Not set)
	PREWDTIME (5)	Maximum charging time during pre-charge phase 0: 30 minutes 1: 60 minutes	1 (60 minutes)
	WDTIME (7, 6)	Maximum charging time after pre-charge phase 00: 1.0 hour 01: 2.0 hours 10: 4.0 hours 11: 6.0 hours	11 (6.0 hours)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
MBCC2 (44 _{HEX})	VBATCOND (1,0)	V _{BATCOND} level setting 00: 2.70V 01: 2.85V 10: 3.00V 11: 3.15V	01 (V _{BATCOND} = 2.85V)
	VMAX (5,2)	V _{MAX} level setting: 0000: 4.00V 0001: 4.02V 0010: 4.04V 0011: 4.06V ... 1010: 4.20V 1011: 4.22V ... 1111: 4.30V	1010 (4.2Volt)
	VRESDBTIME (7)	Debounce time for V _{RES} 0: 32 seconds 1: 64 seconds	1 (64 seconds debounce time)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
MBCC3 (45 _{HEX})	PRECHG CUR (7,0)	Pre-charge current level: 0000 0000: $0/255 * I_{SET}$ 0000 0001: $1/255 * I_{SET}$... 1111 1110: $254/255 * I_{SET}$ 1111 1111: $255/255 * I_{SET}$	00000000 ($0/255 * I_{SET}$, 0mA)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
MBCC4 (46 _{HEX})	FSTCHG CUR1 (7,0)	Fast charge current level: 0000 0000: $0/255 * I_{SET}$ 0000 0001: $1/255 * I_{SET}$... 1111 1110: $254/255 * I_{SET}$ 1111 1111: $255/255 * I_{SET}$	00000000 ($0/255 * I_{SET}$: 0mA)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
MBCC5 (47 _{HEX})	FSTCHG CUR2 (7,0)	Fast charge current level (USB): 0000 0000: $0/255 * I_{SET}$ 0000 0001: $1/255 * I_{SET}$... 1111 1110: $254/255 * I_{SET}$ 1111 1111: $255/255 * I_{SET}$	00000000 ($0/255 * I_{SET}$: 0mA)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
MBCC6 (48 _{HEX})	CUTOFF CUR (4,0)	Cutoff current level, used for battery full detection in CV mode: 00000: $1/32 * I_{FSTCHG CUR}$ 00001: $2/32 * I_{FSTCHG CUR_T}$... 11110: $31/32 * I_{FSTCHG CUR}$ 11111: $32/32 * I_{FSTCHG CUR}$	00000 ($0/32 * I_{FSTCHG CUR}$: 0mA)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
MBCC7 (49 _{HEX})	USBDEVSTAT (1,0)	USB device status setting: 00: configured for 100mA 01: configured for 500mA 10: configured for 1000mA 11: suspend	01 (USB = 500mA)
	BATTEMPENA (2)	If set, battery temperature is measured and has impact on MBC state machine	1 (Set, battery temperature impacts state machine)
	BATSYSIMAX (7,6)	Maximum BAT-SYS current in ideal diode mode: 00: 1.6 A 01: 1.8 A 10: 2.0 A 11: 2.2 A	11 (2.2A)

Register (Address)	Symbol (Bit)	Possible options	Requested behaviour
MBCC8 (4A _{HEX})	NTCLVT (3,0)	Defines the NTC low voltage threshold level in 50mV steps: 0000: 0.60V 0001: 0.65V ... 1110: 1.30V 1111: 1.35V	1011 (1.15V)
	USBENASUS	If set, the USB-System switch is on when USB is in suspend mode.	0 (Not set, USB-System switch is OFF when USB is in suspend mode)